

<b>Form 1449 (Modified)</b>		<b>Atty Docket No.</b>	NWISP046
<b>Information Disclosure Statement By Applicant</b>  Use Several Sheets if Necessary		<b>Application No.:</b>	10/607,819
		<b>Applicant</b>	Kota et al.
		<b>Filing Date</b>	June 27, 2003
		<b>Group</b>	2153
		<b>Confirmation No.</b>	8385
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### U.S. Patent Documents

<b>Examiner Initial</b>	<b>No.</b>	<b>Patent No.</b>	<b>Date</b>	<b>Patentee</b>	<b>Class</b>	<b>Sub-class</b>	<b>Filing Date</b>
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	3.	2001/037435	11/01/01	Van Doren			3/21/01
	4.	20010014097	08/16/01	Beck et al.			12/31/98
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#### Foreign Patent or Published Foreign Patent Application

<b>Examiner Initial</b>		<b>Document No.</b>	<b>Publication Date</b>	<b>Country or Patent Office</b>	<b>Class</b>	<b>Sub-Class</b>	<b>Translation</b>	
					Yes	No		
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	72.	EP0978781	2/9/00	EPO			X	

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<b>Examiner Initial</b>	<b>No.</b>	<b>Author, Title, Place (e.g. Journal) of Publication, Date</b>
	73.	D. E. Culler, J. P. Singh, A. Gupta, "Parallel Computer Architecture", 1999 Morgan Kaufmann, San Francisco, CA USA XP002277658

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	77.	US Office Action mailed September 21, 2005 for US Patent Application No. 10/157,384 [NWISP012]
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